



SGM4684

Chip Scale Packaging, Low-Voltage 0.4Ω, Dual, SPDT Analog Switch

GENERAL DESCRIPTION

The SGM4684 is a dual, low on-resistance, low voltage, bidirectional, single-pole/double-throw (SPDT) CMOS analog switch designed to operate from a single 1.8V to 5.5V power supply. Targeted applications include battery powered equipment that benefit from low R_{ON} (0.4Ω) and fast switching speeds ($t_{ON} = 25\text{ns}$, $t_{OFF} = 28\text{ns}$).

The on resistance profile is very flat over the full analog signal range. This ensures excellent linearity and low distortion when switching audio signals.

The SGM4684 is a committed dual single-pole/double-throw (SPDT) that consist of two normally open (NO) and two normally close (NC) switches. This configuration can be used as a dual 2-to-1 multiplexer.

SGM4684 is available in Green WLCSP-2.0×1.5-10B package.

APPLICATIONS

Battery-Powered, Handheld, and Portable Equipment

Cellular/Mobile Phones

Laptops, Notebooks, Palmtops

Communication Systems

Sample-and-Hold Circuits

Audio Signal Routing

Audio and Video Switching

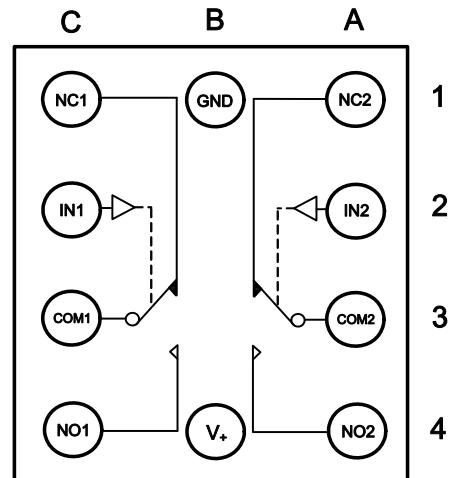
Portable Test and Measurement

Medical Equipment

FEATURES

- Low Voltage Operation: 1.8V to 5.5V
- Low On-Resistance: 0.4Ω (TYP)
- Low On-Resistance Flatness
- -3dB Bandwidth: 13MHz
- Fast Switching Times
 - t_{ON} 25ns
 - t_{OFF} 28ns
- Rail-to-Rail Operation
- Typical Power Consumption (< 0.01μW)
- TTL/CMOS Compatible
- Chip Scale Packaging

PIN CONFIGURATION (TOP VIEW)



WLCSP-2.0×1.5-10B

FUNCTION TABLE

LOGIC	NC1, NC2	NO1, NO2
0	ON	OFF
1	OFF	ON

SGM4684**Chip Scale Packaging, Low-Voltage
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MODEL	PIN-PACKAGE	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKAGE OPTION
SGM4684	WLCSP-2.0×1.5-10B	-40°C to +125°C	SGM4684XG/TR	4684XG	Tape and Reel, 3000

ABSOLUTE MAXIMUM RATINGS

V ₊ to GND.....	-0.3V to 6V	Storage Temperature Range.....	-65°C to +150°C
Analog, Digital voltage range ⁽¹⁾	-0.3V to (V ₊) + 0.3V	Lead Temperature (soldering, 10s).....	260°C
Continuous Current NO, NC, or COM.....	±300mA	ESD Susceptibility	
Peak Current NO, NC, or COM	±500mA	HBM.....	2000V
Operating Temperature Range.....	-40°C to +125°C	MM.....	400V
Junction Temperature.....	150°C		

NOTES:

1. Signals on NC, NO, or COM or IN exceeding V₊ will be clamped by internal diodes. Limit forward diode current to maximum current ratings.
2. Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

CAUTION

This integrated circuit can be damaged by ESD if you don't pay attention to ESD protection. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

SGMICRO reserves the right to make any change in circuit design, specification or other related things if necessary without notice at any time. Please contact SGMICRO sales office to get the latest datasheet.

PIN DESCRIPTION

NAME	FUNCTION
V ₊	Power Supply.
GND	Ground
IN1, IN2	Digital Control Pin to Connect the COM Terminal to the NO or NC Terminals.
COM1, COM2	Common Terminal.
NO1, NO2	Normally-Open Terminal.
NC1, NC2	Normally-Closed Terminal.

NOTE: NO, NC and COM terminals may be an input or output.

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ELECTRICAL CHARACTERISTICS

($V_+ = +5V \pm 10\%$, GND = 0V, $T_A = -40^\circ C$ to $+125^\circ C$. Typical values are at $T_A = +25^\circ C$, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	SGM4684		
			+25°C	-40°C to +125°C	UNITS
ANALOG SWITCH					
Analog Signal Range	V_{NO} , V_{NC} , V_{COM}		0	V	MIN
			V_+	V	MAX
On-Resistance	R_{ON}	$0 \leq V_{NO} \text{ or } V_{NC} \leq V_+, I_{COM} = -10mA$, Test Circuit 1	0.4	Ω	TYP
			0.6	1.1	MAX
On-Resistance Match Between Channels	ΔR_{ON}	$0 \leq V_{NO} \text{ or } V_{NC} \leq V_+, I_{COM} = -10mA$, Test Circuit 1	0.04	Ω	TYP
			0.08	0.12	MAX
On-Resistance Flatness	$R_{FLAT(ON)}$	$0 \leq V_{NO} \text{ or } V_{NC} \leq V_+, I_{COM} = -10mA$, Test Circuit 1	0.1	Ω	TYP
			0.15	0.4	MAX
LEAKAGE CURRENTS					
Source OFF Leakage Current	$I_{NC(OFF)}$, $I_{NO(OFF)}$	$V_{NO} \text{ or } V_{NC} = 4.5V/1V$, $V_{COM} = 1V/4.5V$, $V_+ = +5.5V$, Test Circuit 2	± 4	nA	TYP
			± 10	± 1000	MAX
Channel ON Leakage Current	$I_{NC(ON)}$, $I_{NO(ON)}$, $I_{COM(ON)}$	$V_{NO} \text{ or } V_{NC} = V_{COM} = 1V \text{ or } 4.5V$, $V_+ = +5.5V$, Test Circuit 3	± 4	nA	TYP
			± 10	± 1000	MAX
DIGITAL INPUTS					
Input High Voltage	V_{INH}			2.4	V
Input Low Voltage	V_{INL}			0.8	V
Input Current	I_{INL} or I_{INH}	$V_{IN} = V_{INH}$ or V_{INL}	± 0.01	μA	TYP
			± 0.1	± 1	MAX
DYNAMIC CHARACTERISTICS					
Turn-On Time	t_{ON}	$V_{NO} \text{ or } V_{NC} = 3V$, $R_L = 300\Omega$, $C_L = 35pF$, Test Circuit 4	25		ns
Turn-Off Time	t_{OFF}	$V_{NO} \text{ or } V_{NC} = 3V$, $R_L = 300\Omega$, $C_L = 35pF$, Test Circuit 4	28		ns
Charge Injection	Q	$C_L = 1.0nF$, $V_G = 0V$, $R_G = 0\Omega$, Test Circuit 5	3		pC
Break-Before-Make Time Delay	t_D	$V_{NO1} \text{ or } V_{NC1} = V_{NO2} \text{ or } V_{NC2} = 3V$, $R_L = 300\Omega$, $C_L = 35pF$, Test Circuit 6	10		ns
Off Isolation	O_{ISO}	$R_L = 50\Omega$, $C_L = 5pF$, Test Circuit 7	$f = 100kHz$	-70	dB
			$f = 10kHz$	-85	dB
Total Harmonic Distortion	THD	$f = 20Hz \text{ to } 20kHz$, $V_{COM} = 3.5V_{P-P}$, $R_L = 600\Omega$, $C_L = 50pF$	0.07		%
Channel-to-Channel Crosstalk	X_{TALK}	$R_L = 50\Omega$, $C_L = 5pF$, Test Circuit 8	$f = 100kHz$	-100	dB
			$f = 10kHz$	-105	dB
-3dB Bandwidth	BW	$R_L = 50\Omega$, $C_L = 5pF$, Test Circuit 9	13		MHz
Source OFF Capacitance	$C_{NC(OFF)}$, $C_{NO(OFF)}$			94	pF
Channel ON Capacitance	$C_{NC(ON)}$, $C_{NO(ON)}$, $C_{COM(ON)}$			450	pF
POWER REQUIREMENTS					
Power Supply Current	I_+	$V_+ = +5.5V$, $V_{IN} = 0V$ or $5V$	0.001		μA
				1	MAX

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ELECTRICAL CHARACTERISTICS

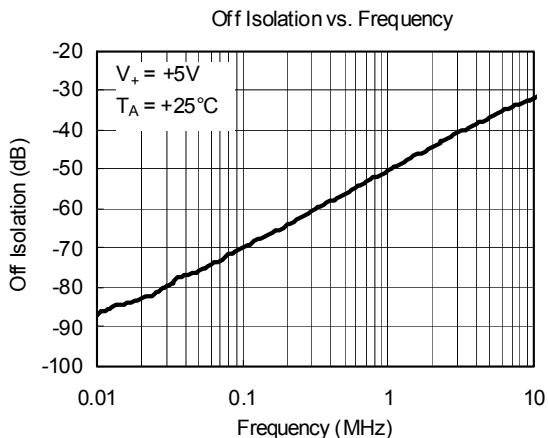
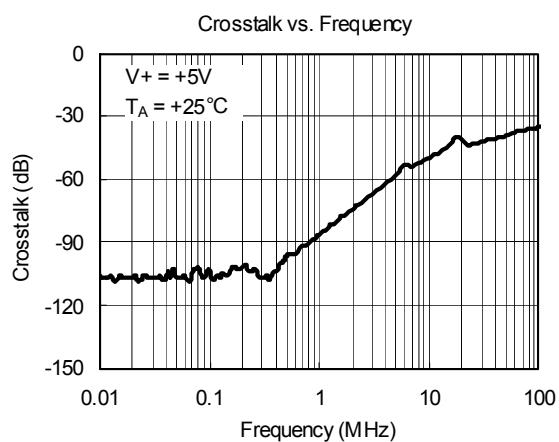
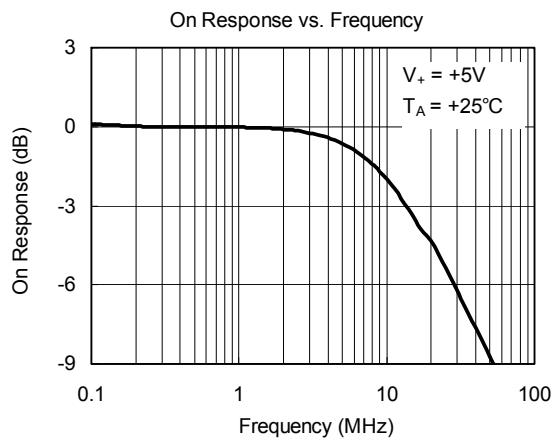
($V_+ = +3V \pm 10\%$, GND = 0V, $T_A = -40^\circ C$ to $+125^\circ C$. Typical values are at $T_A = +25^\circ C$, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	SGM4684			
			+25°C	-40°C to +125°C	UNITS	MIN/MAX
ANALOG SWITCH						
Analog Signal Range	V_{NO} , V_{NC} , V_{COM}		0	V	MIN	
			V_+	V	MAX	
On-Resistance	R_{ON}	$0 \leq V_{NO} \text{ or } V_{NC} \leq V_+$, $I_{COM} = -10mA$, Test Circuit 1	0.6		Ω	TYP
			1.0	1.3	Ω	MAX
On-Resistance Match Between Channels	ΔR_{ON}	$0 \leq V_{NO} \text{ or } V_{NC} \leq V_+$, $I_{COM} = -10mA$, Test Circuit 1	0.05		Ω	TYP
			0.1	0.13	Ω	MAX
On-Resistance Flatness	$R_{FLAT(ON)}$	$0 \leq V_{NO} \text{ or } V_{NC} \leq V_+$, $I_{COM} = -10mA$, Test Circuit 1	0.25		Ω	TYP
			0.3	0.4	Ω	MAX
LEAKAGE CURRENTS						
Source OFF Leakage Current	$I_{NC(OFF)}$, $I_{NO(OFF)}$	$V_{NO} \text{ or } V_{NC} = 3V/1V$, $V_{COM} = 1V/3V$, $V_+ = +3.3V$, Test Circuit 2	±5		nA	TYP
			±11	±1000	nA	MAX
Channel ON Leakage Current	$I_{NC(ON)}$, $I_{NO(ON)}$, $I_{COM(ON)}$	$V_{NO} \text{ or } V_{NC} = V_{COM} = 1V \text{ or } 3V$, $V_+ = +3.3V$, Test Circuit 3	±5		nA	TYP
			±11	±1000	nA	MAX
DIGITAL INPUTS						
Input High Voltage	V_{INH}			2.0	V	MIN
Input Low Voltage	V_{INL}			0.4	V	MAX
Input Current	I_{INL} or I_{INH}	$V_{IN} = V_{INH}$ or V_{INL}	±0.01		μA	TYP
			±0.1	±1	μA	MAX
DYNAMIC CHARACTERISTICS						
Turn-On Time	t_{ON}	$V_{NO} \text{ or } V_{NC} = 2V$, $R_L = 300Ω$, $C_L = 35pF$, Test Circuit 4	30		ns	TYP
Turn-Off Time	t_{OFF}	$V_{NO} \text{ or } V_{NC} = 2V$, $R_L = 300Ω$, $C_L = 35pF$, Test Circuit 4	32		ns	TYP
Charge Injection	Q	$C_L = 1.0nF$, $V_G = 0V$, $R_G = 0Ω$, Test Circuit 5	5		pC	TYP
Break-Before-Make Time Delay	t_D	$V_{NO1} \text{ or } V_{NC1} = V_{NO2} \text{ or } V_{NC2} = 2V$, $R_L = 300Ω$, $C_L = 35pF$, Test Circuit 6	11		ns	TYP
Off Isolation	O_{ISO}	$R_L = 50Ω$, $C_L = 5pF$, Test Circuit 7	$f = 100kHz$	-70		TYP
			$f = 10kHz$	-85		TYP
Total Harmonic Distortion	THD	$f = 20Hz \text{ to } 20kHz$, $V_{COM} = 2V_{P-P}$, $R_L = 600Ω$, $C_L = 50pF$	0.065		%	TYP
Channel-to-Channel Crosstalk	X_{TALK}	$R_L = 50Ω$, $C_L = 5pF$, Test Circuit 8	$f = 100kHz$	-100		TYP
			$f = 10kHz$	-105		TYP
-3dB Bandwidth	BW	$R_L = 50Ω$, $C_L = 5pF$, Test Circuit 9	13		MHz	TYP
Source OFF Capacitance	$C_{NC(OFF)}$, $C_{NO(OFF)}$			94		pF
Channel ON Capacitance	$C_{NC(ON)}$, $C_{NO(ON)}$, $C_{COM(ON)}$			450		pF
POWER REQUIREMENTS						
Power Supply Current	I_+	$V_+ = +3.3V$, $V_{IN} = 0V \text{ or } 3V$	0.001		μA	TYP
			1		μA	MAX

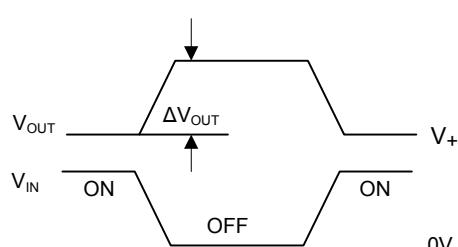
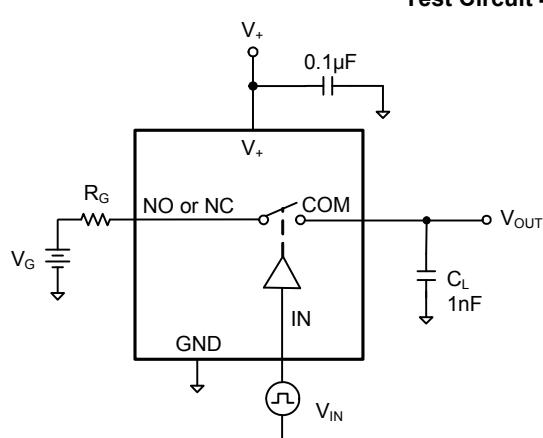
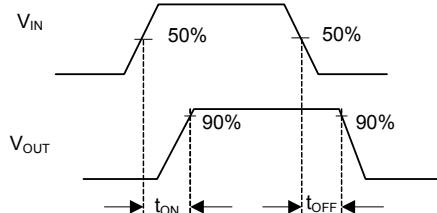
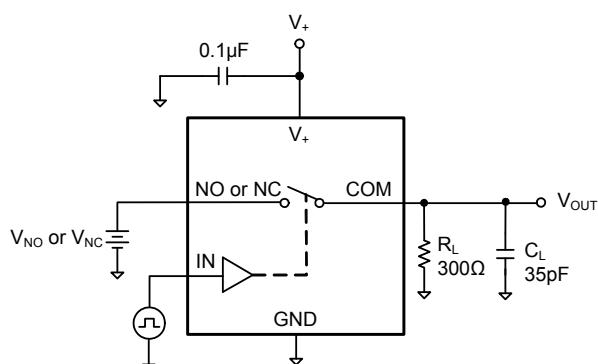
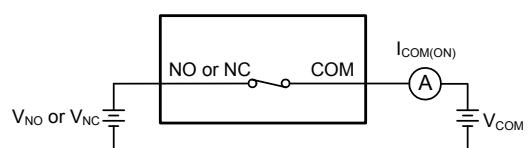
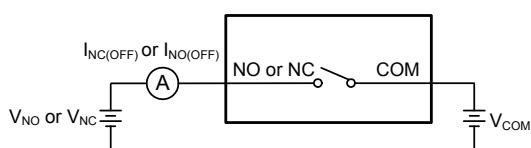
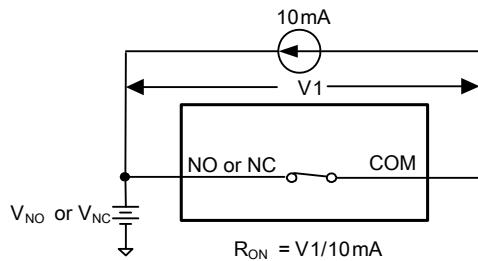
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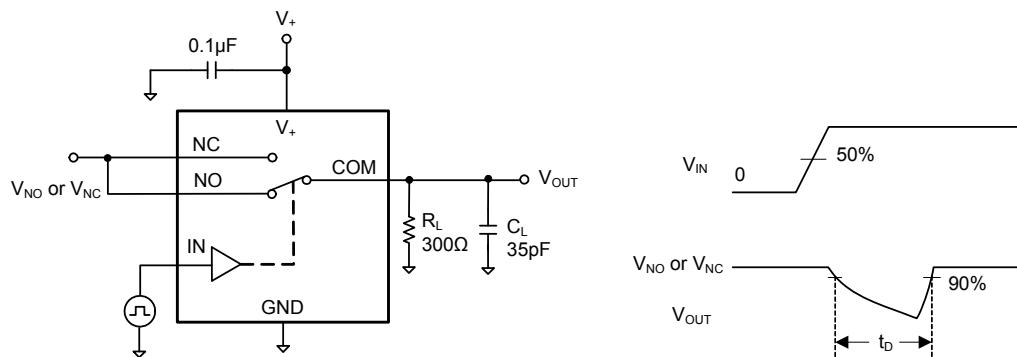
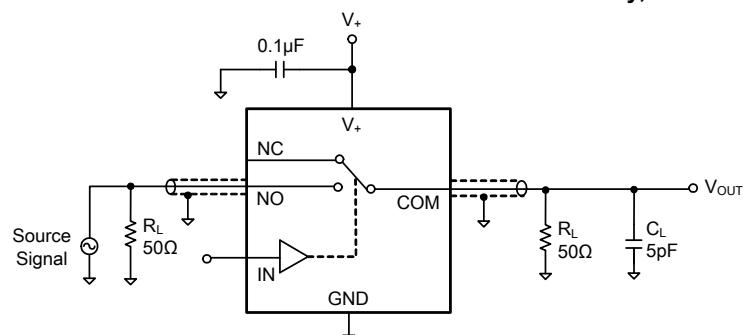
TYPICAL PERFORMANCE CHARACTERISTICS



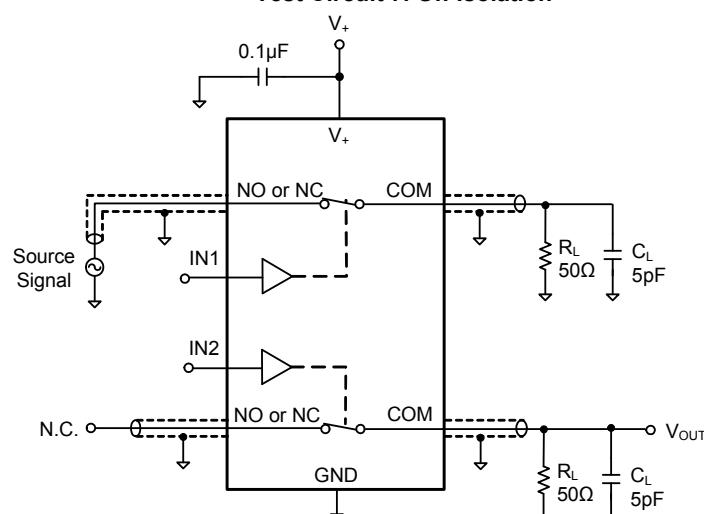
TEST CIRCUITS



TEST CIRCUITS (Cont.)

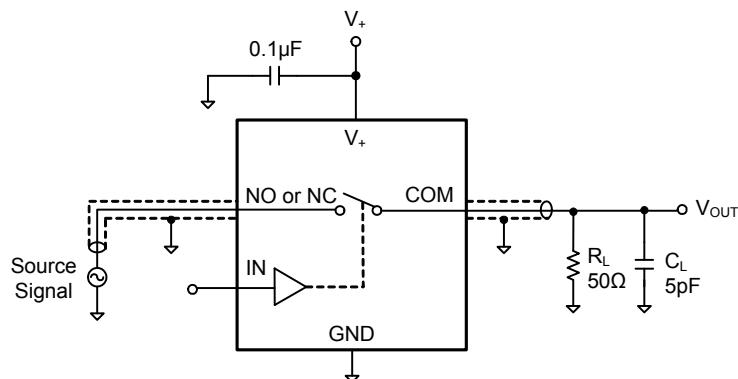
Test Circuit 6. Break-Before-Make Time Delay, t_D 

Test Circuit 7. Off Isolation



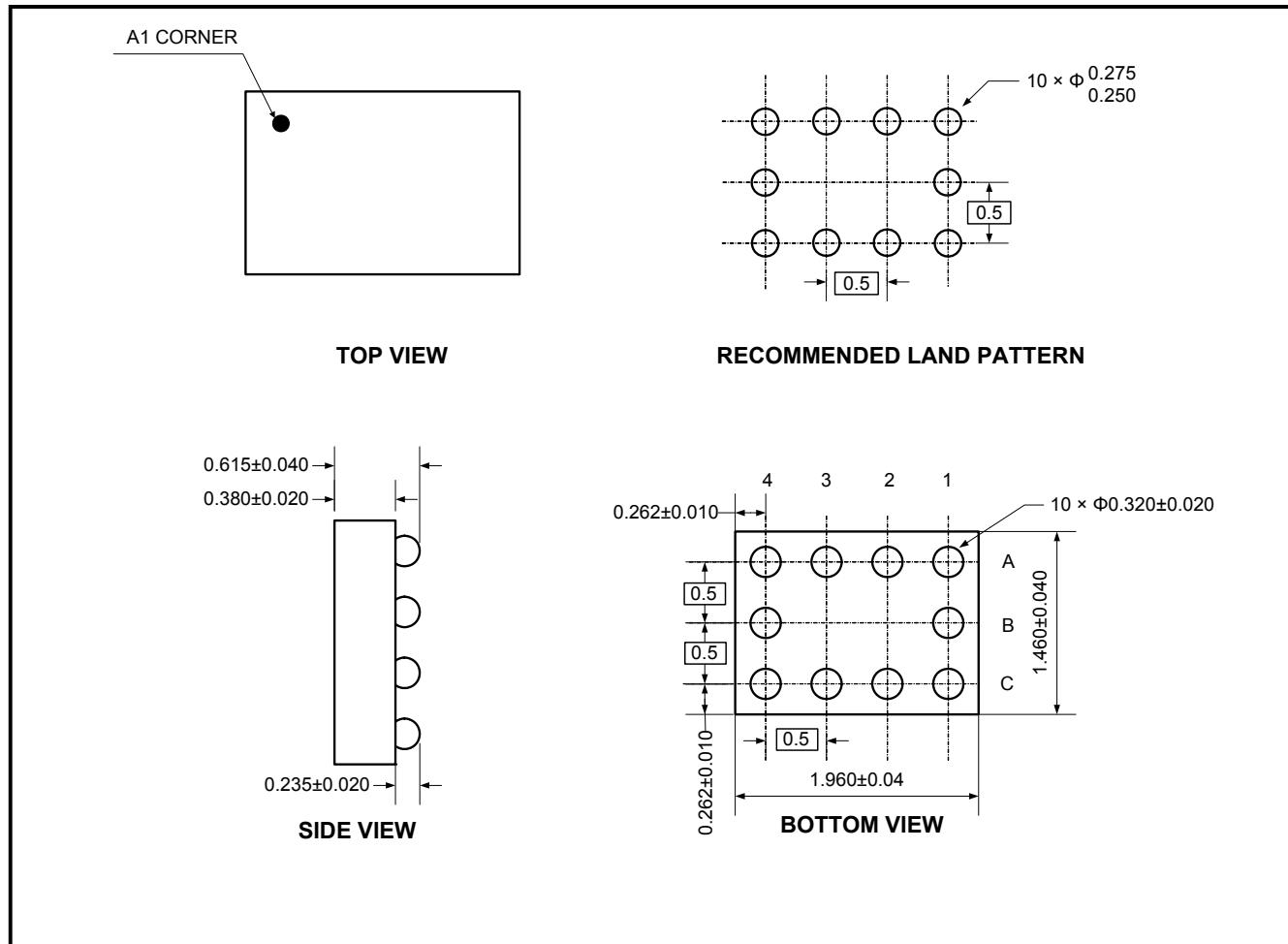
$$\text{Channel To Channel Crosstalk} = -20 \times \log \frac{V_{NO} \text{ or } V_{NC}}{V_{OUT}}$$

Test Circuit 8. Channel-to-Channel Crosstalk

TEST CIRCUITS (Cont.)**Test Circuit 9. -3dB Bandwidth**

PACKAGE OUTLINE DIMENSIONS

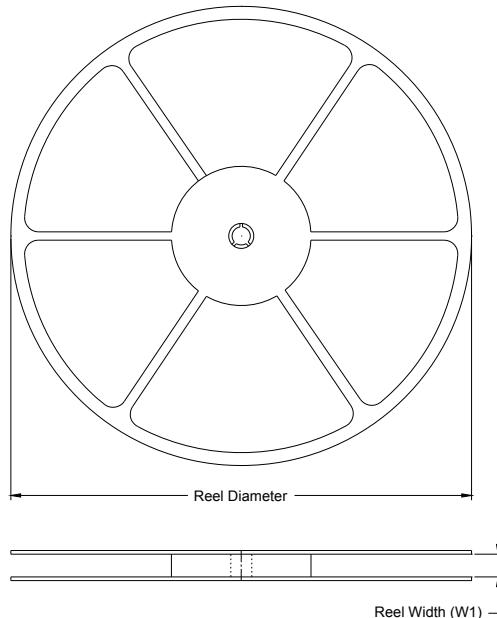
WLCSP-2.0×1.5-10B



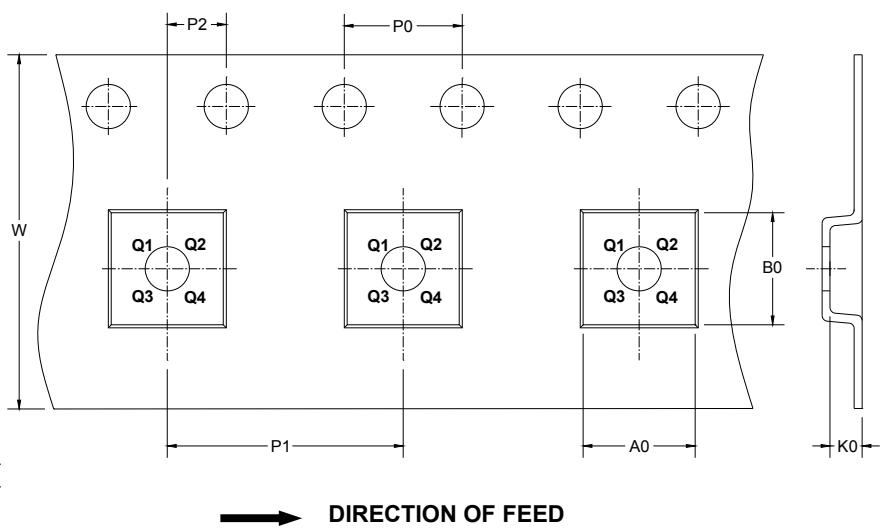
NOTE: All linear dimensions are in millimeters.

TAPE AND REEL INFORMATION

REEL DIMENSIONS



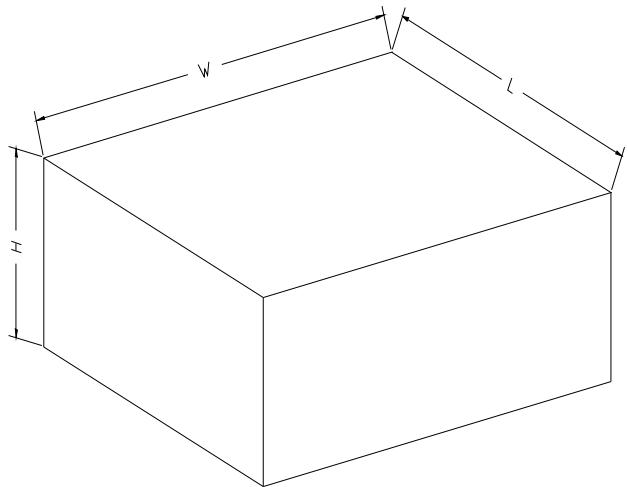
TAPE DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
WLCSP-2.0×1.5-10B	7"	9.2	1.7	2.2	0.9	4.0	4.0	2.0	8.0	Q2

CARTON BOX DIMENSIONS

NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7" (Option)	368	227	224	8
7"	442	410	224	18